Fig. 8
PRIOR ART
DIAGRAM ILLUSTRATING
FORMATS OF PACKETS

	E0P ~ P1	2bit	EOP ~P2	2bit	CRC16 EOP ~P3	16bit 2bit		
	CRC5	Sbit	CRC5	5b it	•			
	Vumber	it	ENDP	4bit	DATA		P4	
	Frame Number	11bit	ADDR	7bit	DATA		EOP ~P4	2bit
	PID	8bit	PIO	8bit	PIO	8bit	PIO	8bit
	SYNC	8bit	SYNC	8bit	SYNC	Bbit	SYNC	8bit
SOF PACKET		Taken phoket		DATA PAPOLET		HANDSHAKE PACKET		

Fig. 9

3

PRIOR ART

DIAGRAM FOR DESCRIBINGT OPERATIONS IN OCCURRENCE OF ERROR OF DEVICE

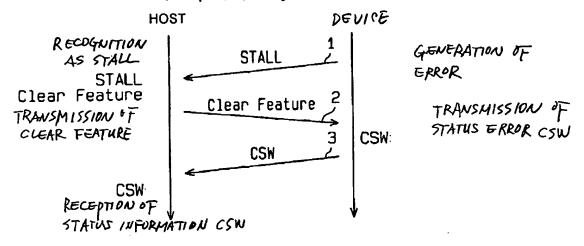


Fig. 10 PRIOR ART

DIAGRAM ILLUSTRATING
OPERATIONS IN OCCURRENCE OF
EPRONEOS PECOGNITION

